

Refine Search

Search Results -

Terms	Documents
semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	0

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L14

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, November 21, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=TDBD; PLUR=YES; OP=ADJ</i>		
<u>L14</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L14</u>
	<i>DB=DWPI; PLUR=YES; OP=ADJ</i>		
<u>L13</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L13</u>
	<i>DB=JPAB; PLUR=YES; OP=ADJ</i>		
<u>L12</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L12</u>
	<i>DB=EPAB; PLUR=YES; OP=ADJ</i>		
<u>L11</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L11</u>
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<u>L10</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	1	<u>L10</u>
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		

<u>L9</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric))	3	<u>L9</u>
<u>L8</u>	semiconductor and ((adjust\$ or alter\$) same (flatband adj voltage) same (high adj k adj dielectric)).clms	0	<u>L8</u>
<u>L7</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric)).clms	0	<u>L7</u>
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L6</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L5</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L4</u>
<i>DB=PGPB; PLUR=YES; OP=ADJ</i>			
<u>L3</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L3</u>
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>			
<u>L2</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L2</u>
<i>DB=DWPI; PLUR=YES; OP=ADJ</i>			
<u>L1</u>	semiconductor and (modulat\$ same (flatband adj voltage) same (high adj k adj dielectric))	0	<u>L1</u>

END OF SEARCH HISTORY